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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR:	Mukta G. Farooq et al.)	EXAMINER:	Michael Aboagye
)		
SERIAL NO.:	10/711,501)	ART UNIT:	1793
)		
FILING DATE:	September 22, 2004)	DATE:	July 30, 2008
)		
FOR:	SOLDER INTERCONNECTION ARRAY WITH OPTIMAL MECHANICAL INTEGRITY			

AMENDMENT UNDER RULE 312

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Responsive to the Notice of Allowance mailed June 30, 2008, please amend the above-identified application as follows:

Amendments to the Specification begins on page 2.

Remarks begin on page 5.